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APPLICATION NO	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/748,345	12/29/2003	Mark L. Doczy	42P17820	8139	
8791	7590 11/30/2006	ĖXAMINER		IINER	
	Y SOKOLOFF TAYLO	THAI. L	THAI, LUAN C		
12400 WILSHIRE BOULEVARD SEVENTH FLOOR			ART UNIT	PAPER NUMBER	
LOS ANG	LOS ANGELES, CA 90025-1030			2891	
		DATE MAILED: 11/30/2006			

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
Office A. P	10/748,345	DOCZY ET AL.			
Office Action Summary	Examiner	Art Unit			
	Luan Thai	2891			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) daywill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 11 September 2006.					
2a) ☐ This action is FINAL . 2b) ☑ This	☐ This action is FINAL . 2b) ☑ This action is non-final.				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 45	53 O.G. 213.			
Disposition of Claims					
4)⊠ Claim(s) 1,2,4-8,10-16 and 25 is/are pending in the application.					
4a) Of the above claim(s) is/are withdrawn from consideration.					
5) Claim(s) is/are allowed.					
6) Claim(s) <u>1,2,4-8,10-16 and 25</u> is/are rejected.					
7) Claim(s) is/are objected to.		•			
8) Claim(s) are subject to restriction and/or	r election requirement.	·			
Application Papers					
9) The specification is objected to by the Examine	r.				
10)⊠ The drawing(s) filed on <u>29 December 2003</u> is/are: a)⊠ accepted or b)☐ objected to by the Examiner.					
Applicant may not request that any objection to the	= ' '				
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11) The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.			
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).					
a) ☐ All b) ☐ Some * c) ☐ None of:1.☐ Certified copies of the priority documents have been received.					
2. Certified copies of the priority documents have been received in Application No					
3. Copies of the certified copies of the priority documents have been received in this National Stage					
application from the International Bureau	ı (PCT Rule 17.2(a)).				
* See the attached detailed Office action for a list of the certified copies not received.					
Attachment(s) 1) X Notice of References Cited (PTO-892)	A) The latest days Consumers	(DTO 412)			
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) Paper No(s)/Mail Date.					
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 9/11/06.	5) Notice of Informal P 6) Other:	atent Application (PTO-152)			
. Apar 110(0)/111001 Date <u>0/ 11/00</u> .	o,				

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DETAILED ACTION

This Office action is responsive to the amendment filed 9/11/06.

Claims 1-2, 4-8, 10-16 and newly added claim 25 are pending in this application.

Claims 3, 9 and 17-24, have been cancelled.

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-2, 4-5, 7-8, 10-16 and 25, are rejected under 35 U.S.C. 103(a) as being unpatentable over Tsukamoto (6,040,224 of record) in view of Yu (6,495,437 of record).

Regarding claims 1, 4-5, 7, 10, and 25, Tsukamoto (see specifically figure 5C-5E) disclose a method of forming a microelectronic structure comprising: providing a substrate (11) comprising source/drain regions (21) and gate region (18), wherein the gate region comprises a metal layer (15) of tungsten disposed on a gate dielectric layer (13) of silicon dioxide, a polysilicon layer (16) disposed on the metal layer (15), laser annealing the substrate to activate the implanted species (Col. 4, lines 65+). Since the doped polysilicon film (16) is adapted to protect the metal gate layer (15) from the laser annealing (Col. 4, lines 25+), the metal gate layer (15) is considered "not substantially diffuse into the gate dielectric layer". Tsukamoto fails to teach the gate dielectric layer (13) being a high-k dielectric layer.

Yu while related to a similar method of forming a microelectronic structure teaches that the conventional gate oxides, such as silicon dioxide, which is used to form the gate dielectric Application/Control Number: 10/748,345

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layer, being replaced by a high-k dielectric layer, such as tantalum pentaoxide, silicon nitride, titanium oxide, and aluminum oxide (Col. 2, lines 43+), in order to reduce gate leakage (Col. 2, lines 27+). It would have been obvious to a person of ordinary skill in the art at the time the invention was made to recognize that combining Yu's teachings with Tsukamoto's invention would have been beneficial because it helps to reduce gate leakage.

Regarding claims 2, 11, 13-14 and 16, Tsukamoto teaches the metal layer (15) made of tungsten, wherein tungsten is inherent to have a work function about 4eV, as evidenced by Bustos et al. (U.S. 2004/0126977, paragraph [0067]); and thus, the work function of tungsten layer (15) is approximately equal to the work function of a doped polysilicon, as evidenced by Applicant's Specification, paragraph [0018].

Regarding claims 8 and 12, Tsukamoto's figures 5D-5E show the ratio of the depth of the source/drain regions (21) to the length of the source/drain regions (21) being less than about 1:2.

Regarding claim 15, Yu in the proposed method teaches the metal gate layer (36) can be made of Molybdenum (Col. 4, lines 60+), which has a work function between about 5eV and about 5.2eV, as evidenced by Chambers (7,005,365, Col. 6, lines 27+).

3. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Tsukamoto (6,040,224 of record) and Yu (6,495,437 of record), as applied to claim 1 above, and further in view of Goto (6,599,819 of record).

Regarding claim 6, the proposed method of Tsukamoto and Yu discloses the claimed invention as detailed above except for specifying the laser beam pulsed at *about 20 nanoseconds* or less.

Although the proposed method of Tsukamoto and Yu does not specify the claimed time range of the laser beam pulsed (e.g., 20 nanoseconds or less), the annealing time using laser beam is commonly less than 20 ns for activating the implanted regions in a substrate, as disclosed by Goto (Col. 3, lines 49+). It would have been obvious to one of ordinary skill in the art at the time the invention was made to pulse the laser beam to the substrate at about 20 ns or less for activating the implanted regions in the substrate since such the pulsed time of a laser beam is commonly applied in the art, as taught by Goto, and such time range is an art recognized variable of importance which is subject to routine of experimentation and optimization.

4. Claims 1-2, 4-5, 7-8, 10-16 and 25, are rejected under 35 U.S.C. 103(a) as being unpatentable over Zhang (US 2002/0105033 of record) in view of Yu (6,495,437 of record).

Regarding claims 1, 4-5, 7, 10, and 25, Zhang (see specifically figure 1A) disclose a method of forming a microelectronic structure comprising: providing a substrate (1) comprising source/drain regions (24) and gate region (6/8), wherein the gate region comprises a metal layer (8) disposed on a gate dielectric layer (6) (paragraph [0063]), and laser annealing the substrate to activate the implanted species (paragraph [0071]), wherein the metal layer does not substantially diffuse into the gate dielectric layer (paragraphs [0064]-[0065] and [0072]-[0073]). Zhang fails to teach the gate dielectric layer (6) being a high-k dielectric layer.

Yu while related to a similar method of forming a microelectronic structure teaches that the conventional gate oxides, such as silicon dioxide, which is used to form the gate dielectric layer, being replaced by a high-k dielectric layer, such as tantalum pentaoxide, silicon nitride, titanium oxide, and aluminum oxide (Col. 2, lines 43+), in order to reduce gate leakage (Col. 2, lines 27+). It would have been obvious to a person of ordinary skill in the art at the time the

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invention was made to recognize that combining Yu's teachings with Zhang's invention would have been beneficial because it helps to reduce gate leakage.

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Regarding claims 2, 11, 13-14 and 16, Yu in the proposed method teaches the metal gate layer (36) can be made of tungsten (Col. 4, lines 60+), which is inherent to have a work function about 4eV, as evidenced by Bustos et al. (U.S. 2004/0126977, paragraph [0067]); and thus, the work function of the metal layer (36) made of tungsten is approximately equal to the work function of a doped polysilicon, as evidenced by Applicant's Specification, paragraph [0018].

Regarding claims 8 and 12, Zhang's figure 1A shows the ratio of the depth of the source/drain regions (24) to the length of the source/drain regions (24) being less than about 1:2.

Regarding claim 15, Yu in the proposed method teaches the metal gate layer (36) can be made of Molybdenum (Col. 4, lines 60+), which has a work function between about 5eV and about 5.2eV, as evidenced by Chambers (7,005,365, Col. 6, lines 27+).

5. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Zhang (US 2002/0105033 of record) and Yu (6,495,437 of record), as applied to claim 1, and further in view of Goto (6,599,819 of record).

Regarding claim 6, the proposed method of Zhang and Yu discloses the claimed invention as detailed above except for specifying the laser beam pulsed at about 20 nanoseconds or less.

Although the proposed method of Zhang and Yu does not specify the time range of the laser beam pulsed (e.g., 20 nanoseconds or less), the annealing time using laser beam is commonly less than 20 ns for activating the implanted regions in a substrate, as disclosed by Goto (Col. 3, lines 49+). It would have been obvious to one of ordinary skill in the art at the time

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the invention was made to pulse the laser beam to the substrate at about 20 ns or less for

activating the implanted regions in the substrate since such the pulsed time of a laser beam is

commonly applied in the art, as taught by Goto, and such time range is an art recognized variable

of importance which is subject to routine of experimentation and optimization.

6. Any inquiry concerning this communication or earlier communications from the examiner

should be directed to Luan Thai whose telephone number is 571-272-1935. The examiner can normally

be reached on 6:30 AM - 5:00 PM, Monday to Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor,

Bradley W. Baumeister can be reached on 571-272-1722. The fax phone number for the organization

where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application

Information Retrieval (PAIR) system. Status information for published applications may be obtained

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direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the

Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Luan Thai

Primary Examiner Art Unit 2891

May 26, 2006